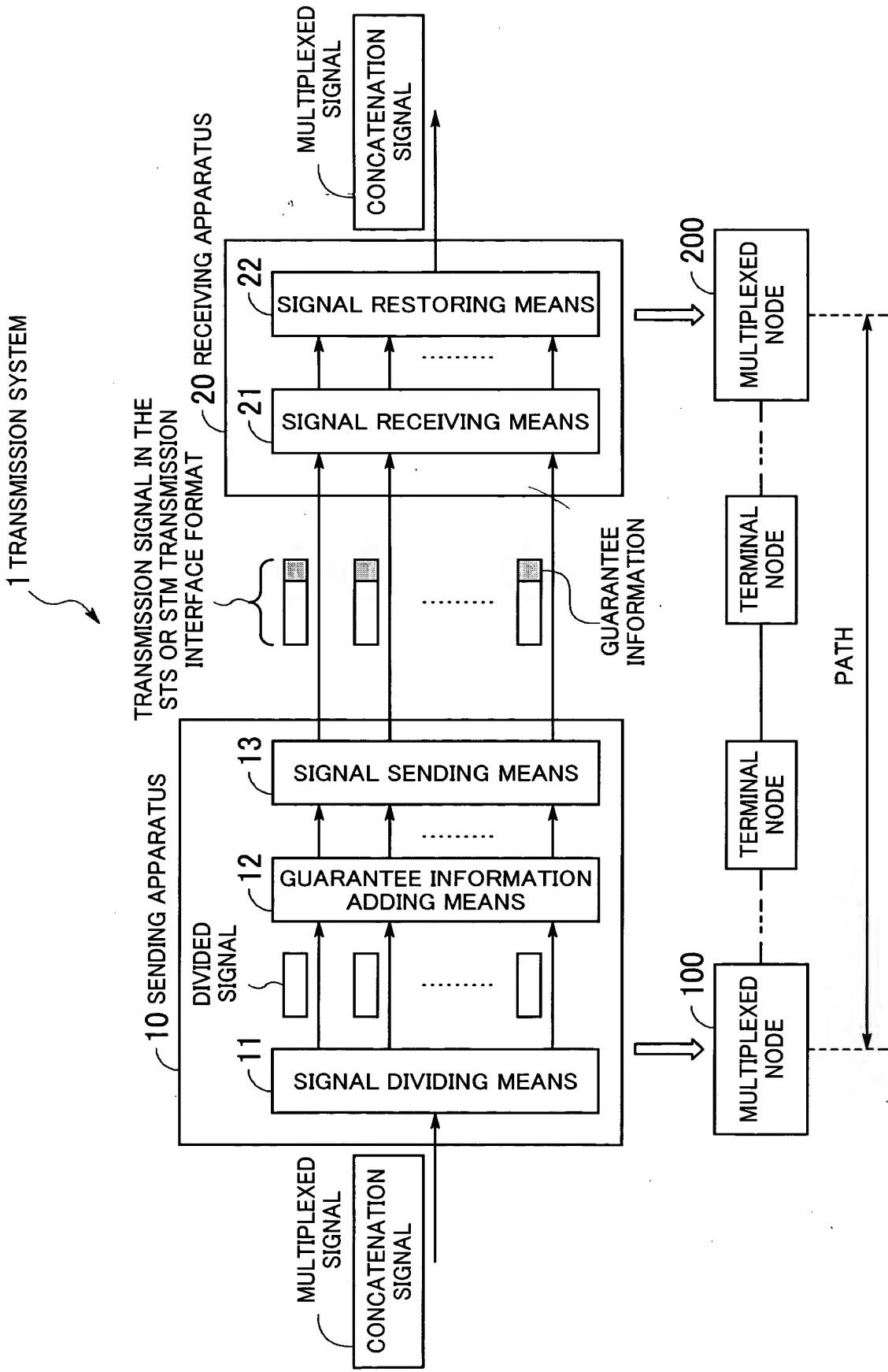


FIG. 1



“F D T C S G D = 8 9 2 3 6 6 5 0

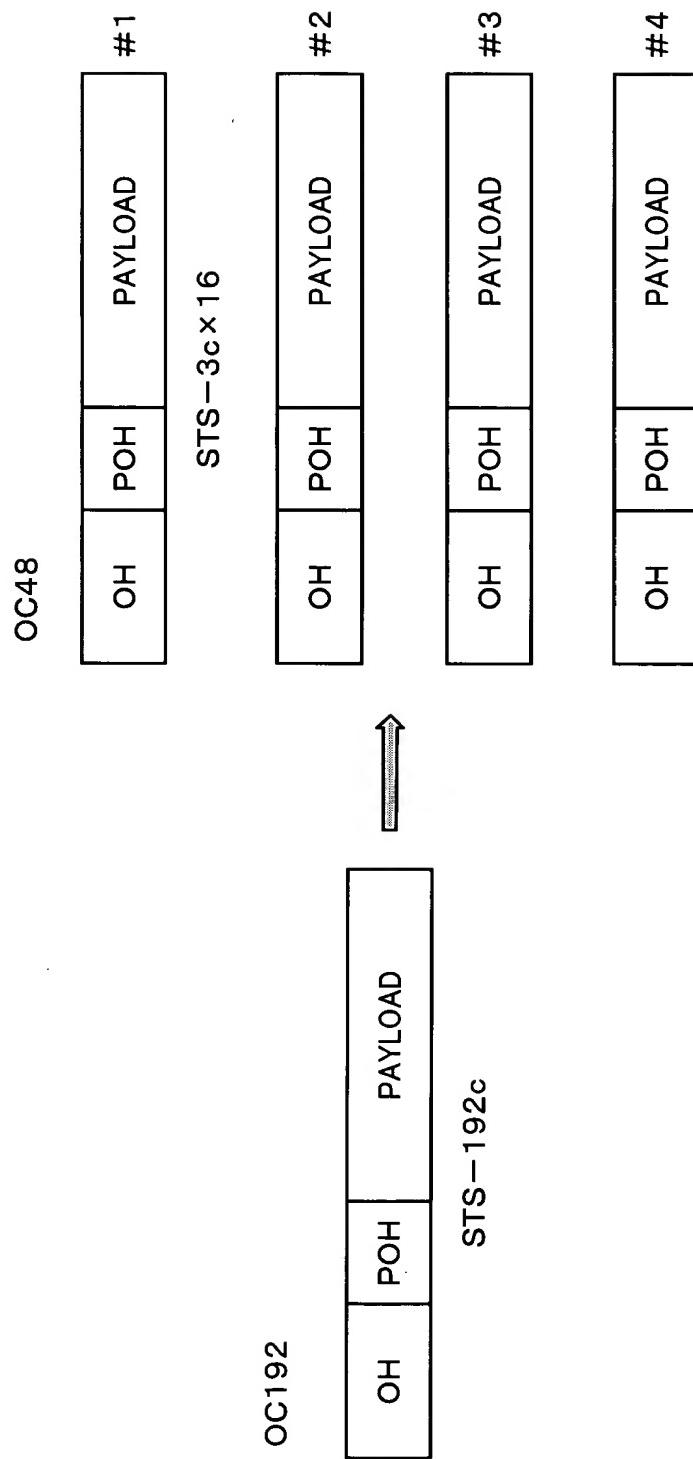


FIG. 2

00000000-0000-0000-0000-000000000000

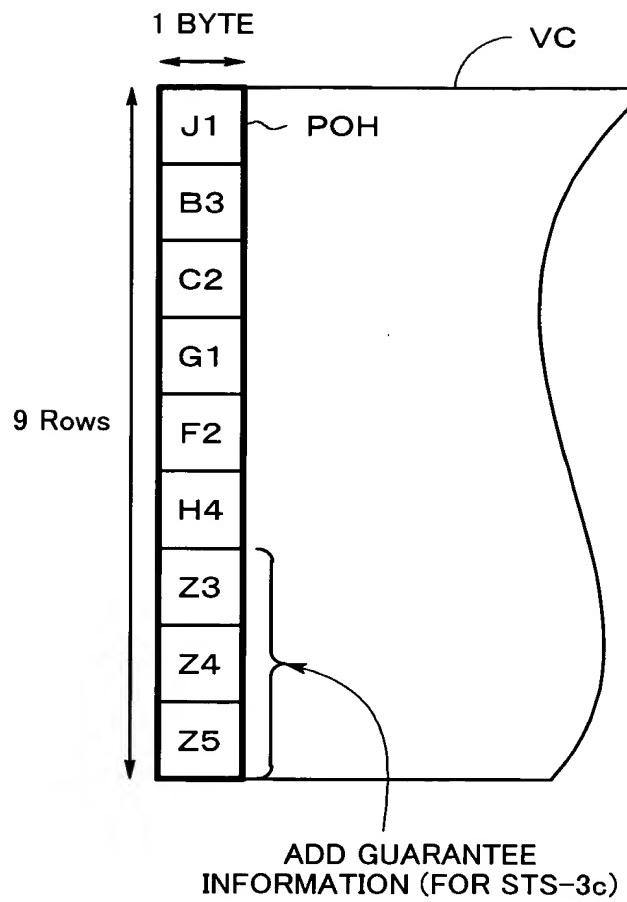


FIG. 3

Y O T E S I O " S S E S S S C D

GUARANTEE INFORMATION

NAME	BYTE USED	BIT USED	CONTENTS
CONCATENATION INFORMATION	Z3	BITS 5-8	INFORMATION REGARDING A CONCATENATION SIGNAL BEFORE DIVISION 0:STS-3c, 1:STS-12c, 2:STS-48c, 3:STS-192c, 4:STS-768c
FRAME INFORMATION	Z4	BITS 1-8	FRAME NUMBER BEFORE DIVISION UP TO 256 FRAME NUMBERS (0-255)
BLOCK INFORMATION	Z5	BITS 1-8	SERIAL NUMBERS ADDED TO DIVIDED PIECES OF DATA IN ORDER OF DIVISION

FIG. 4

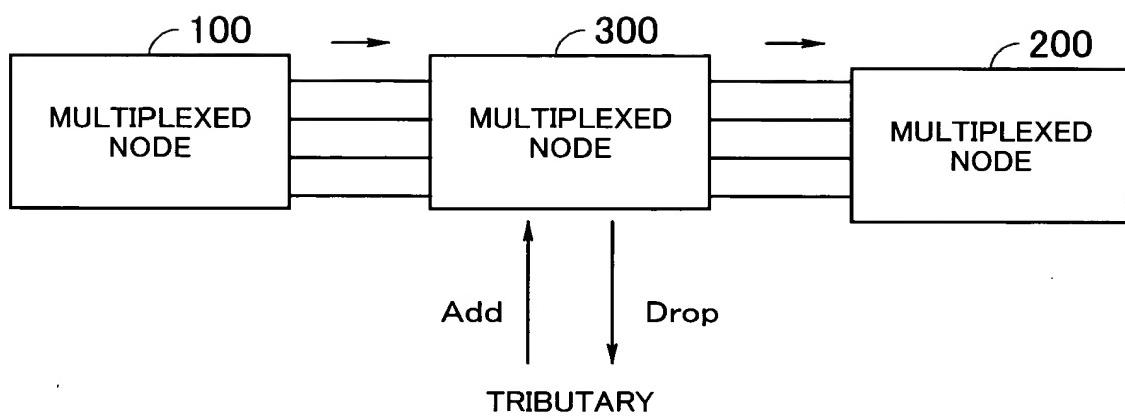


FIG. 5

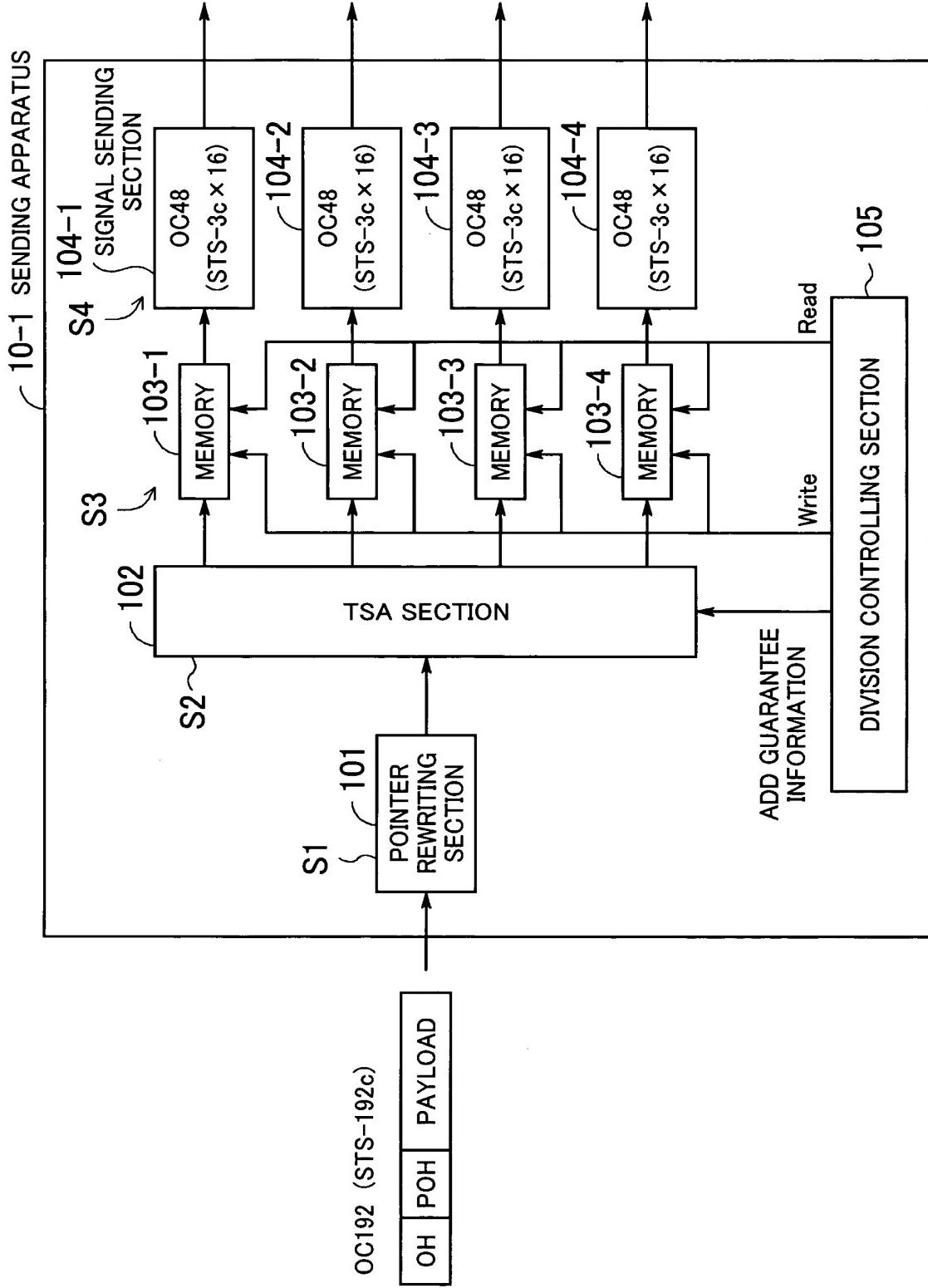


FIG. 6

20-1 RECEIVING APPARATUS

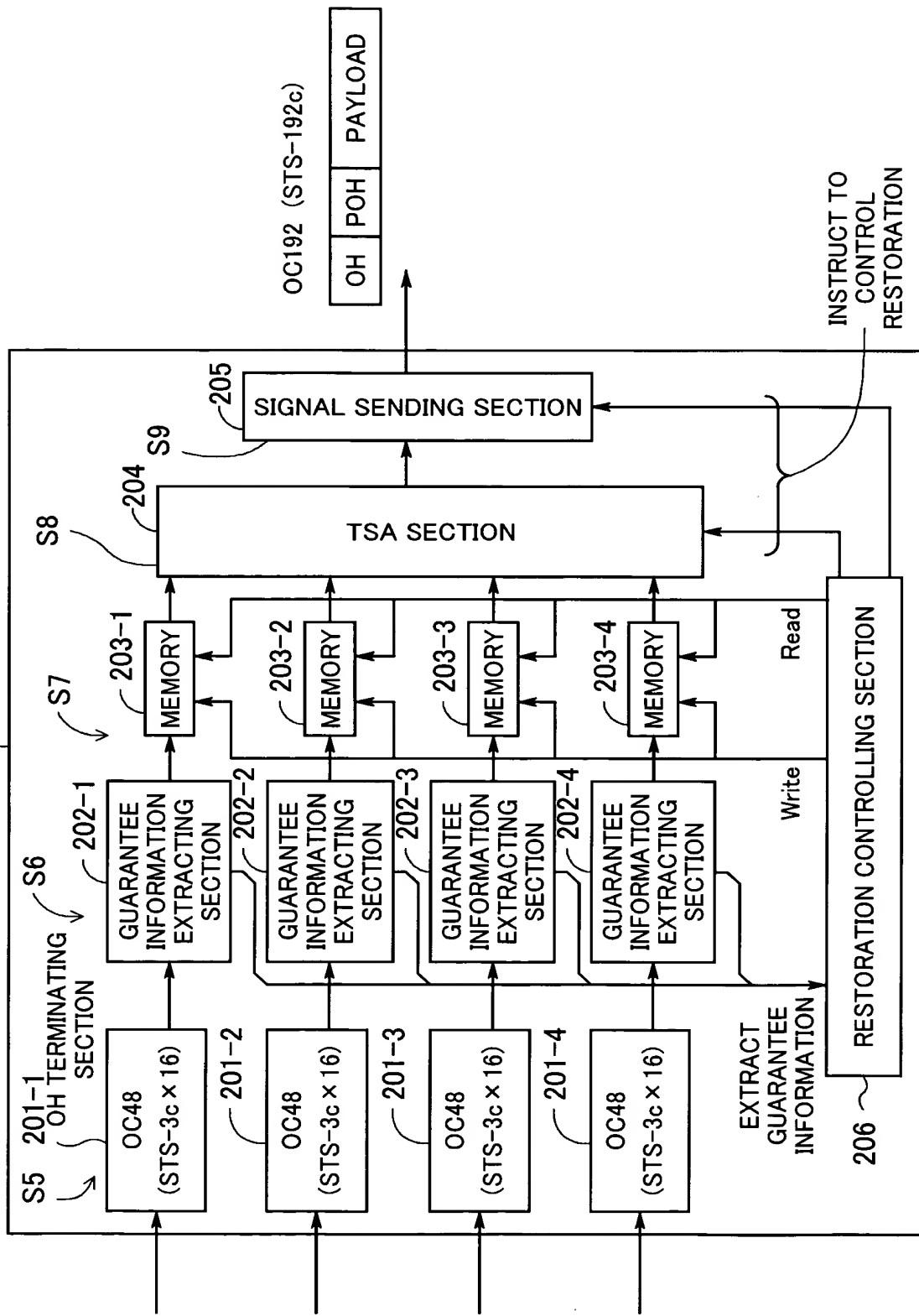


FIG. 7

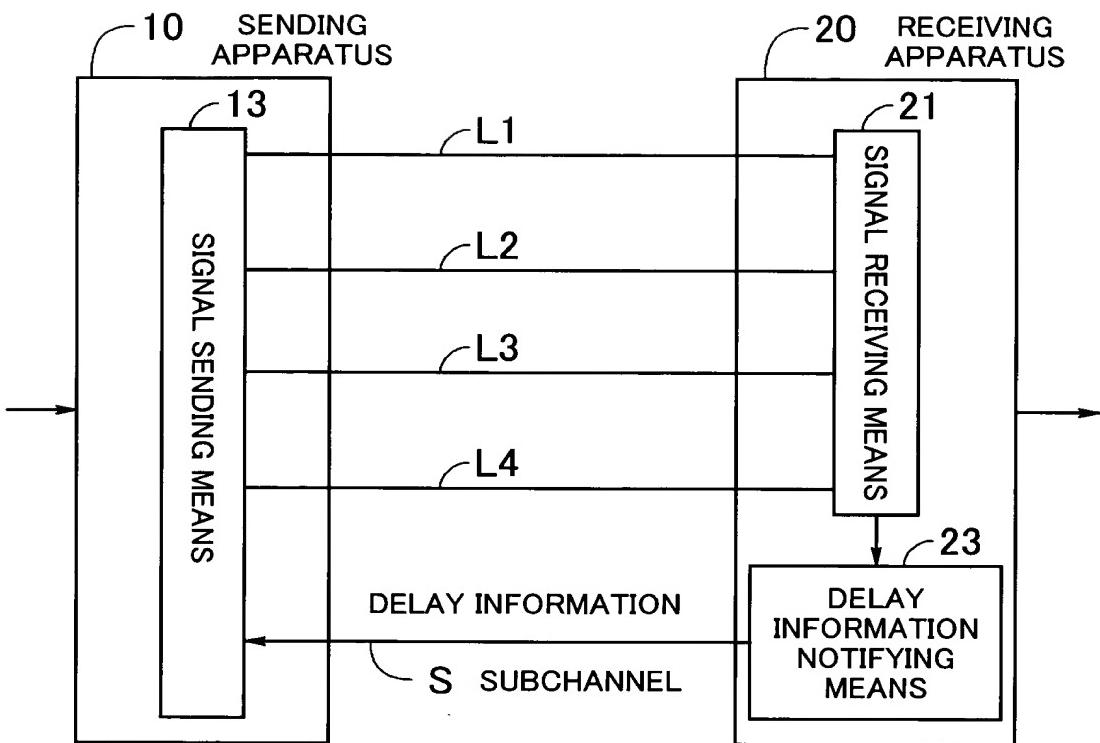
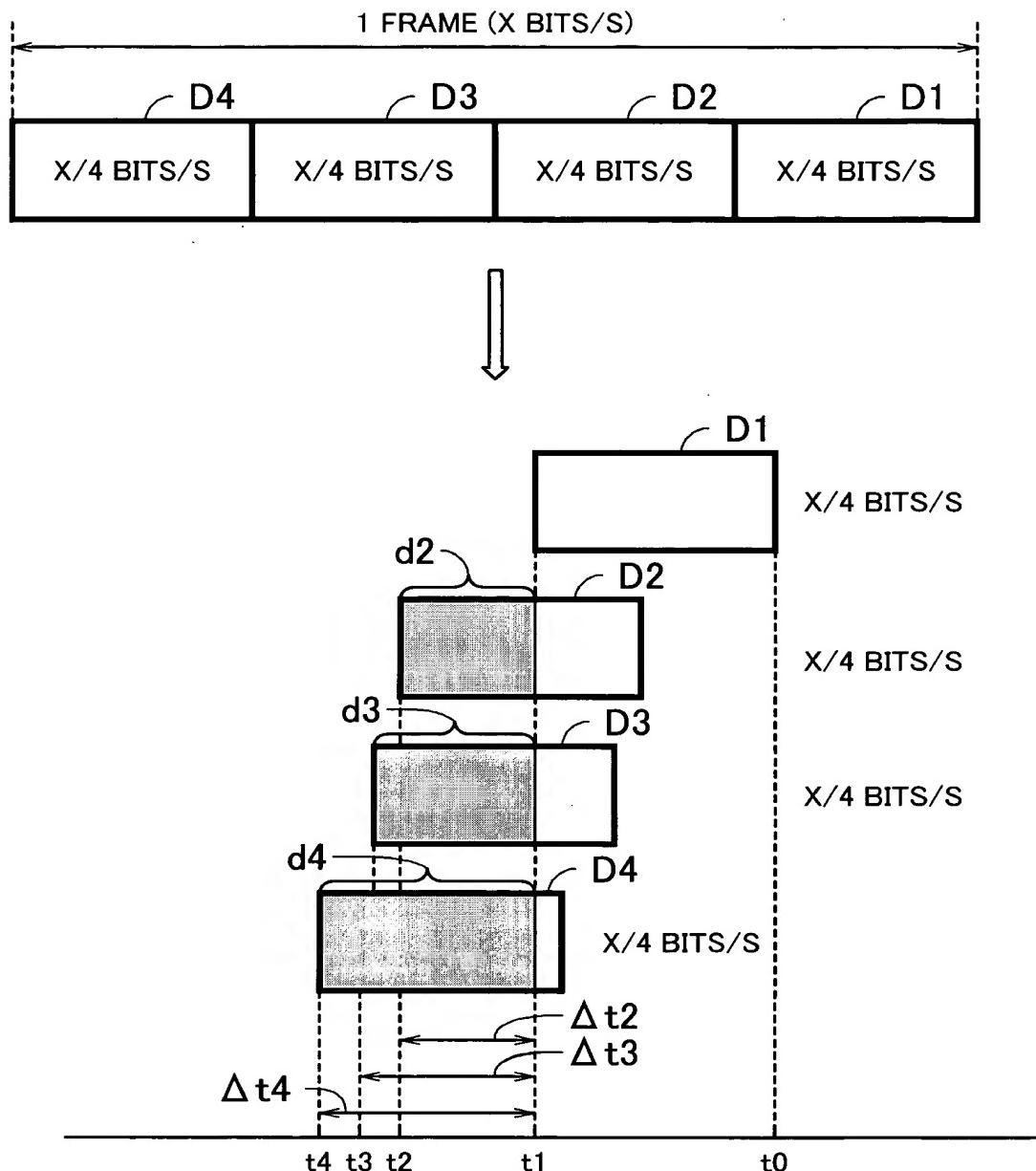


FIG. 8

TOP SECRET - 00000000000



: DATA WHICH DOES NOT REACH BY TIME t_1

FIG. 9

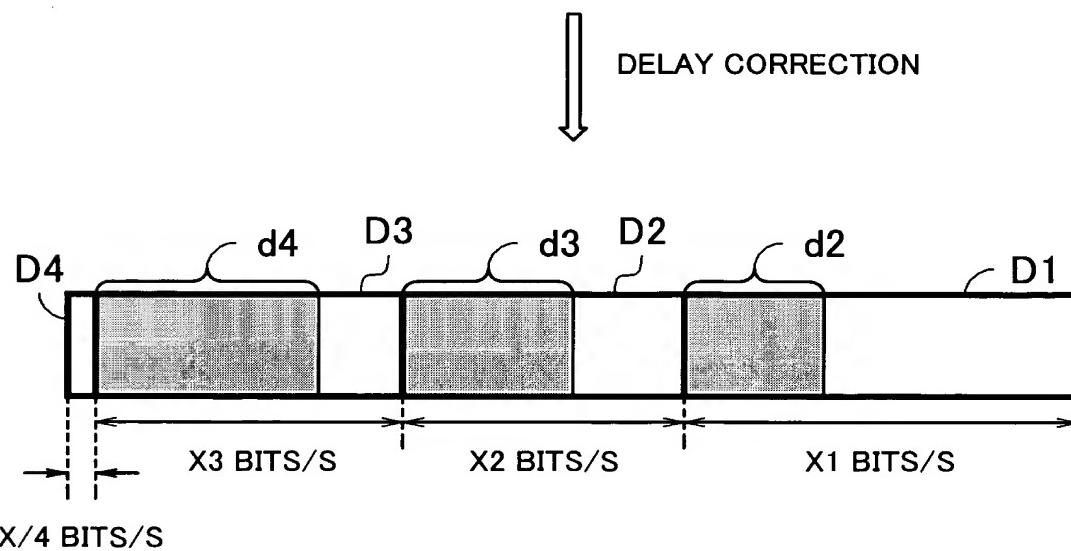
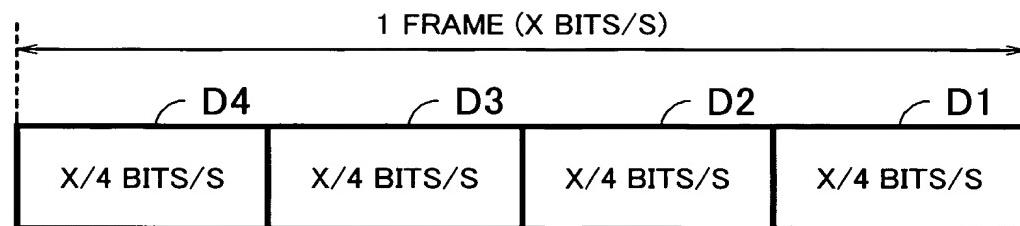


FIG. 10

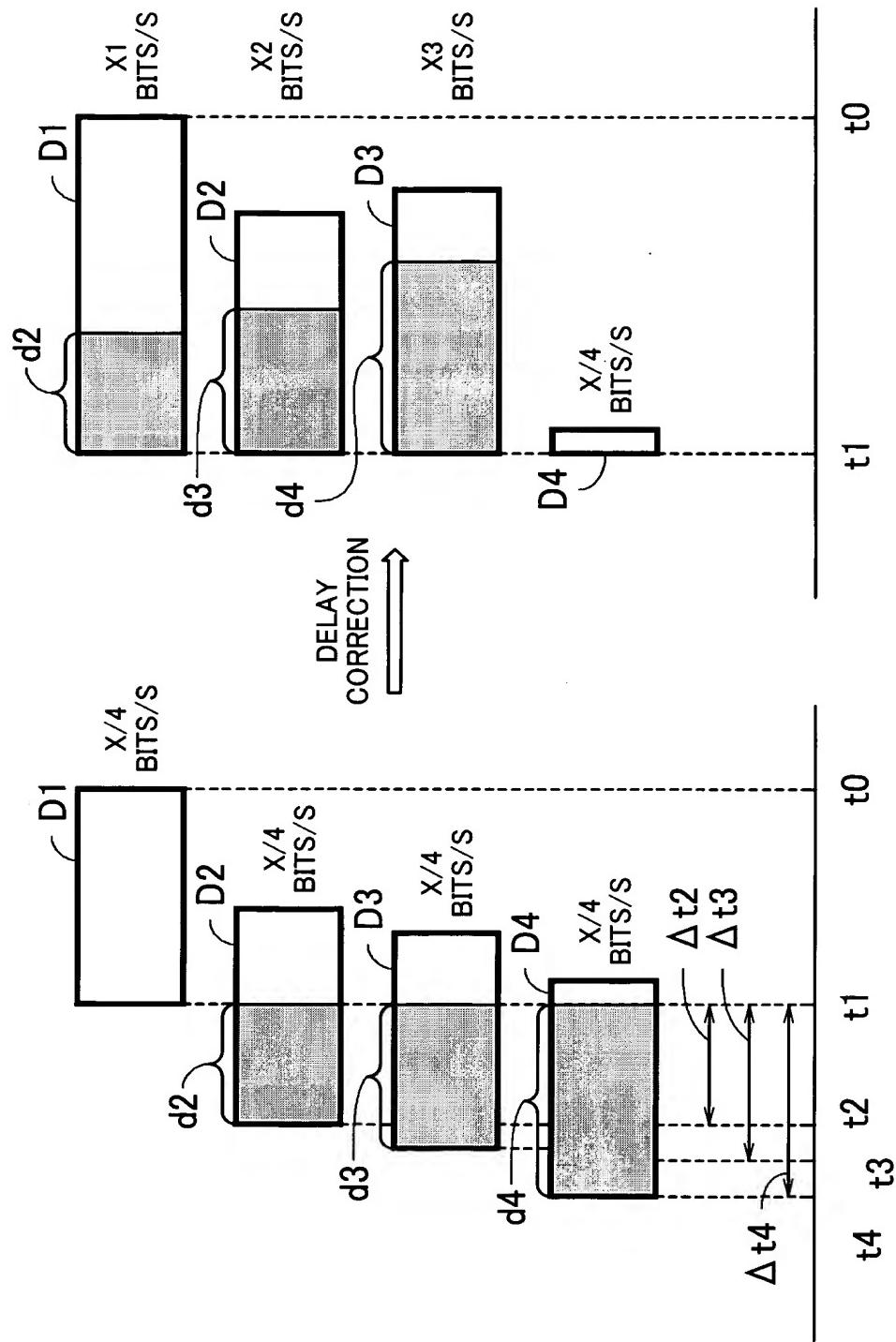


FIG. 11

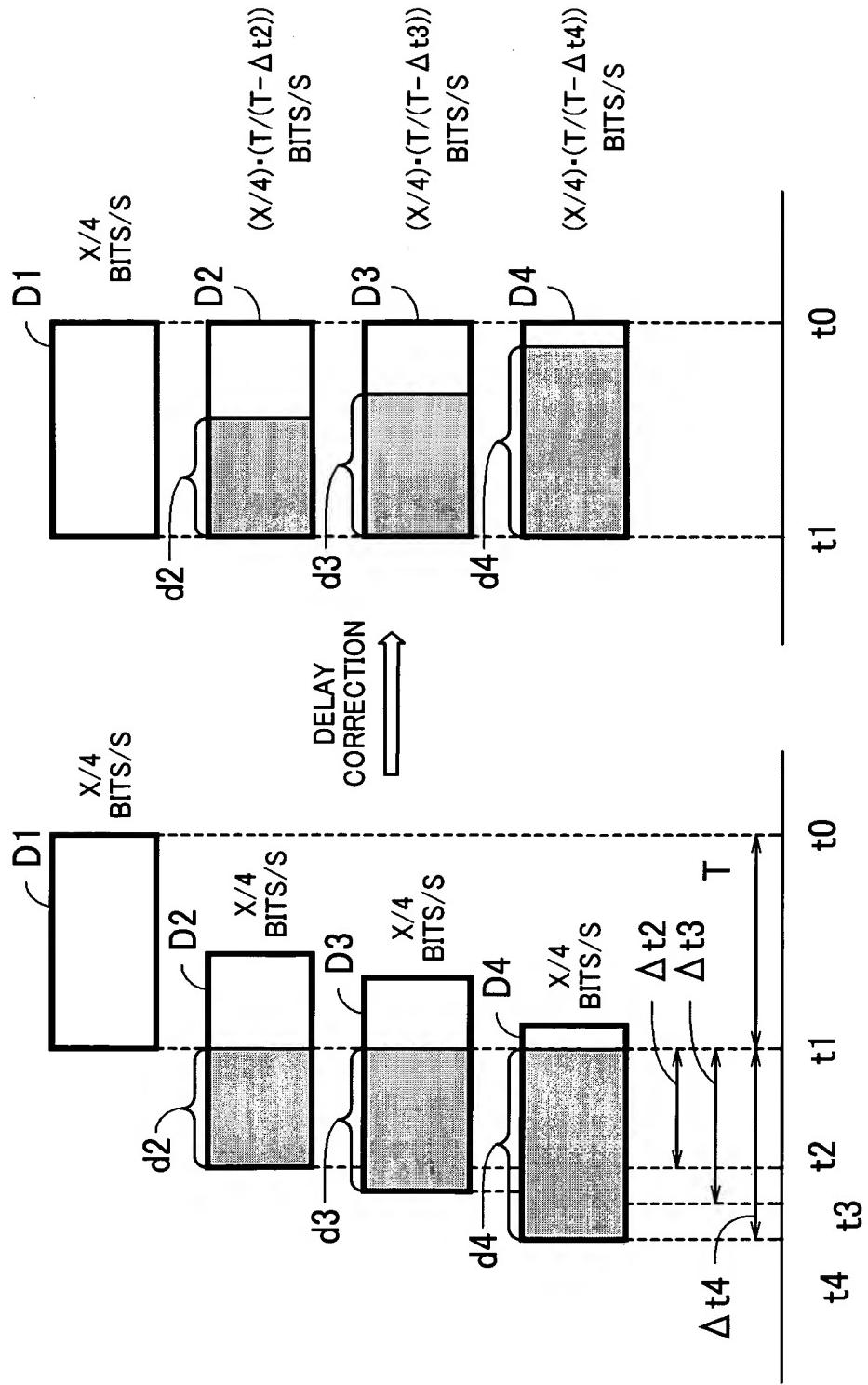


FIG. 12

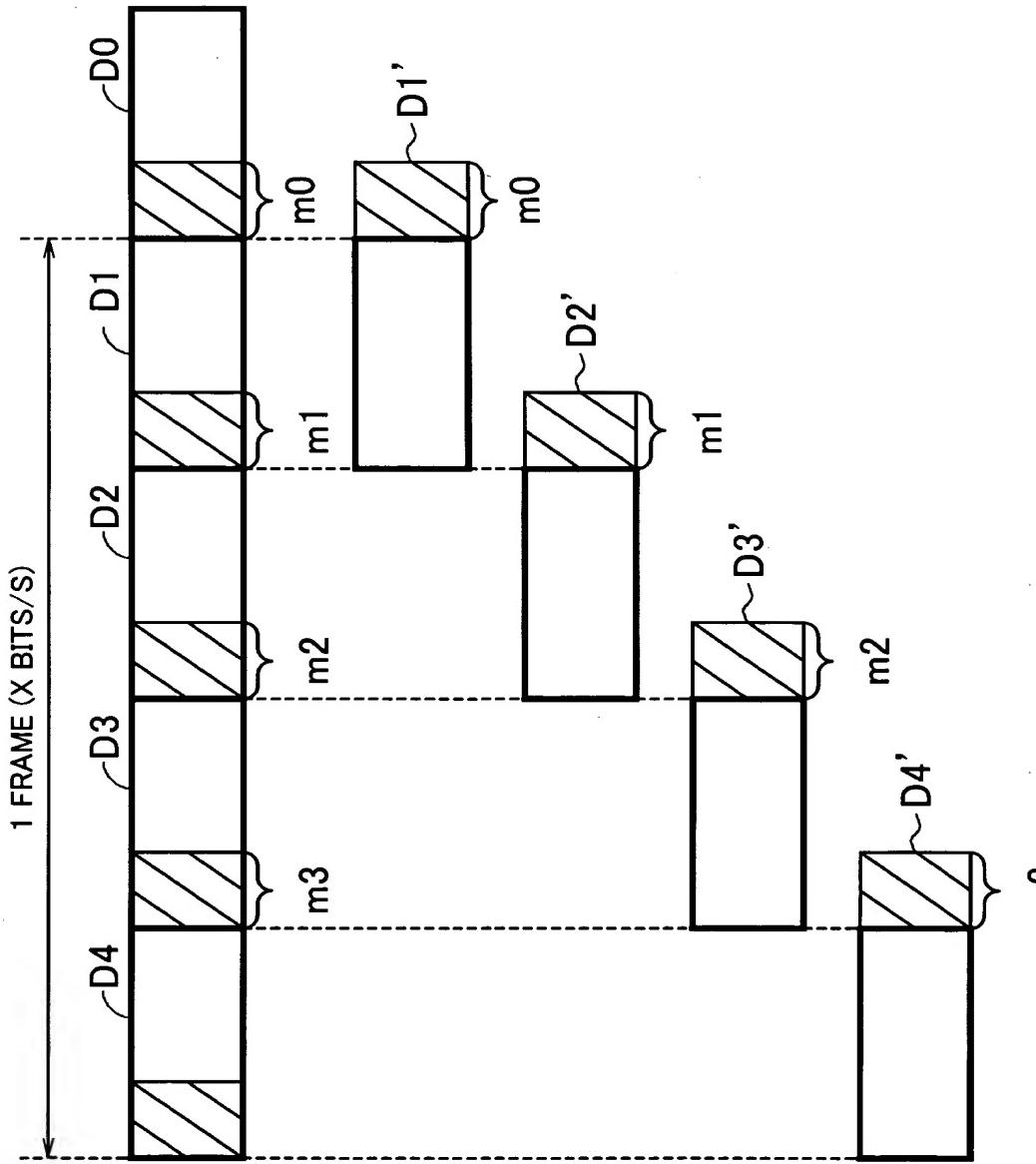


FIG. 13

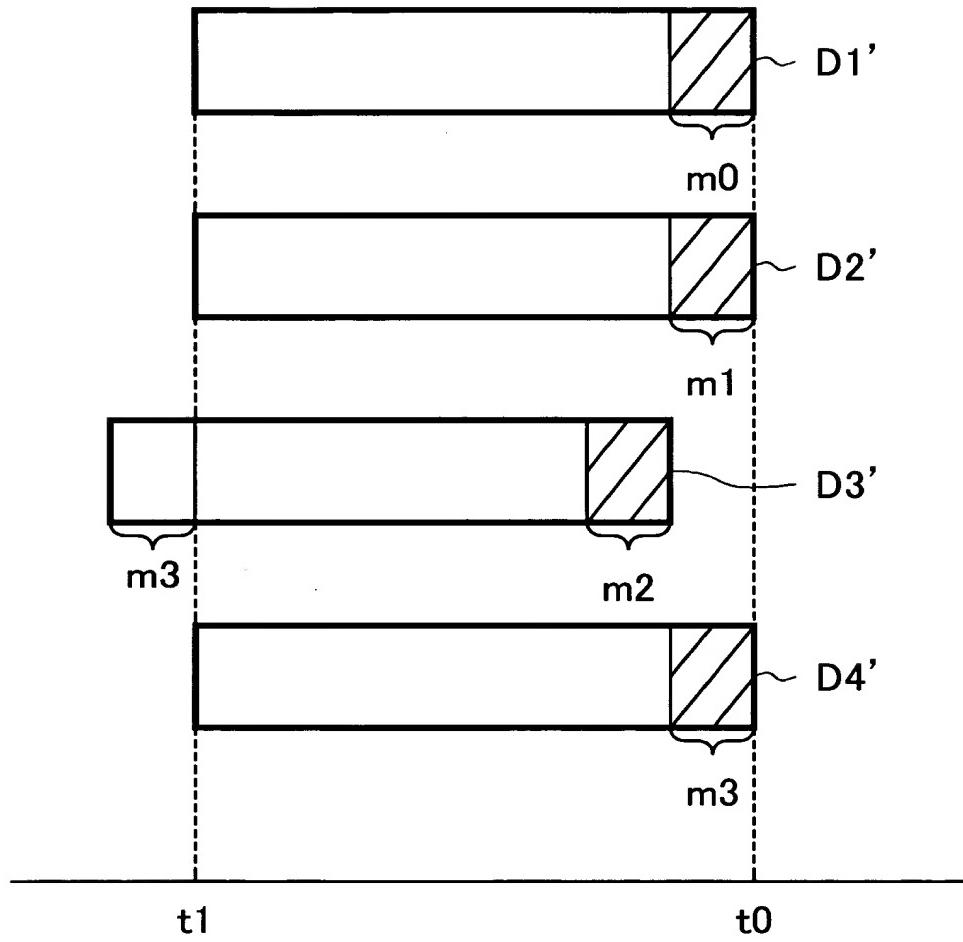


FIG. 14

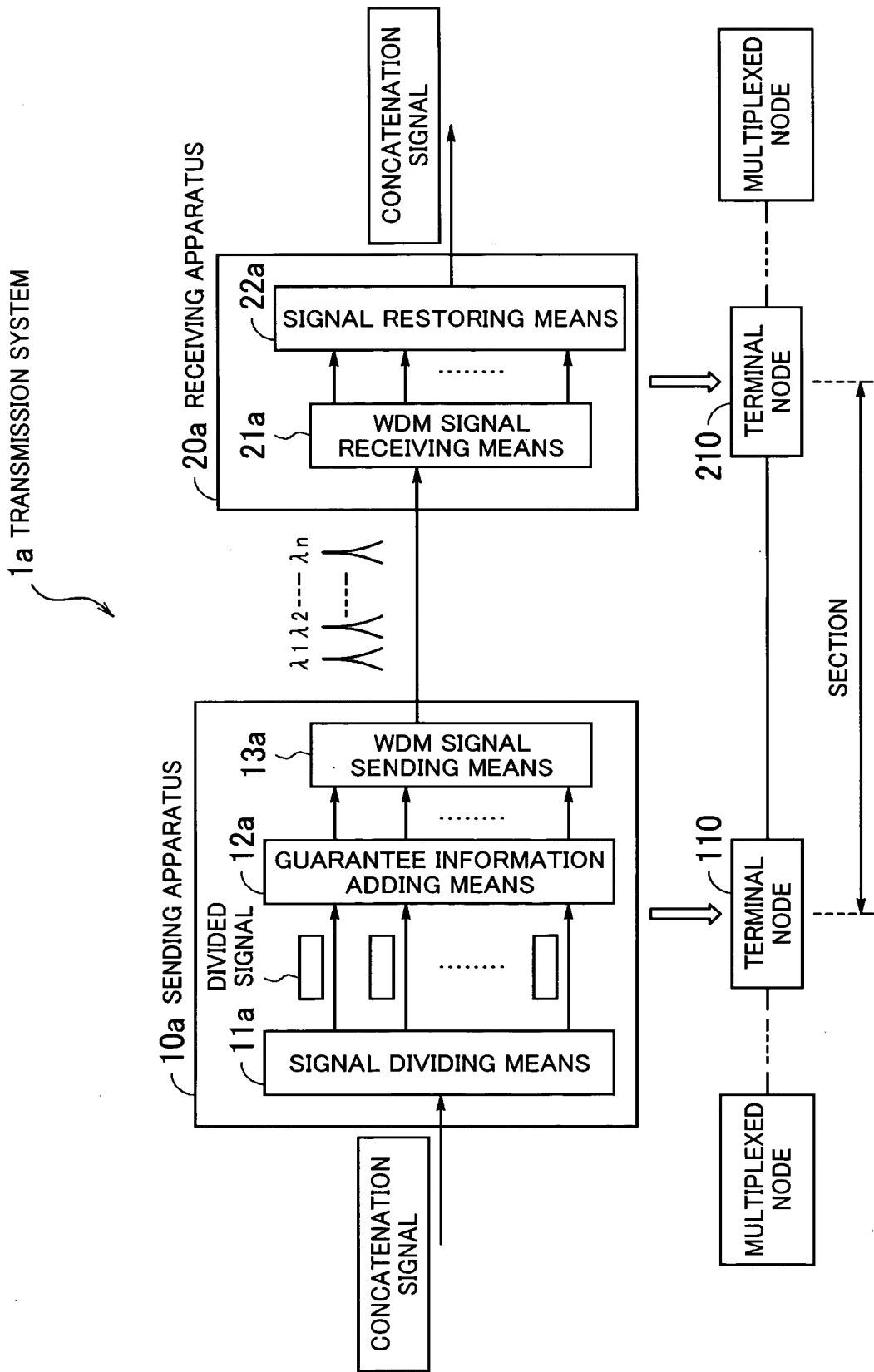


FIG. 15

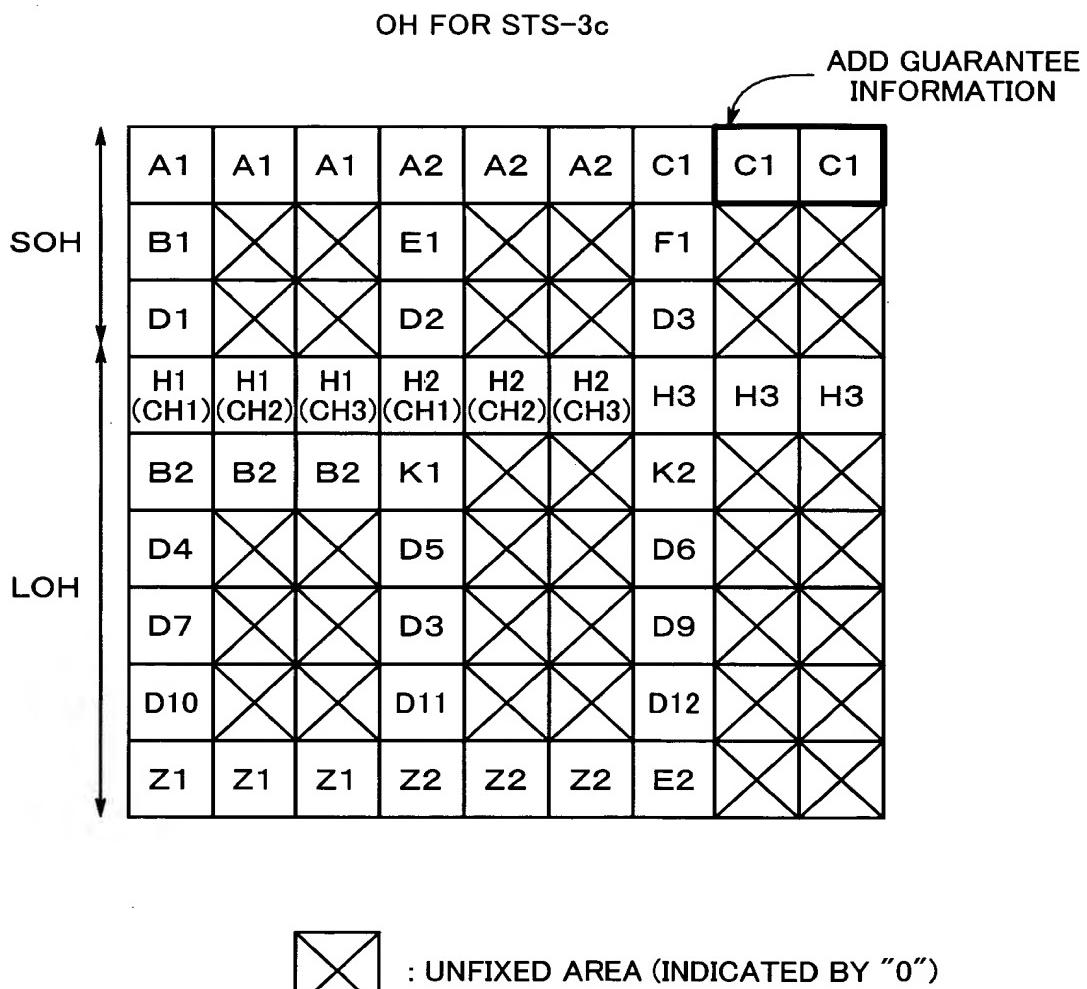


FIG. 16

GUARANTEE INFORMATION

NAME	BYTE USED	BIT USED	CONTENTS
CONCATENATION INFORMATION	C1#2	BITS 1-4	INFORMATION REGARDING A CONCATENATION SIGNAL BEFORE DIVISION 0:STS-3c, 1:STS-12c, 2:STS-48c, 3:STS-192c, 4:STS-768c
FRAME INFORMATION	C1#2	BITS 5-8	FRAME NUMBER BEFORE DIVISION UP TO 16 FRAME NUMBERS (0-15)
BLOCK INFORMATION	C1#3	BITS 1-8	SERIAL NUMBER ADDED TO A DIVIDED PIECE OF DATA IN ORDER OF DIVISION ACCORDING TO WAVELENGTHS, UP TO 256 NUMBERS (0-255)

FIG. 17

FIGURE 10a-1 SENDING APPARATUS

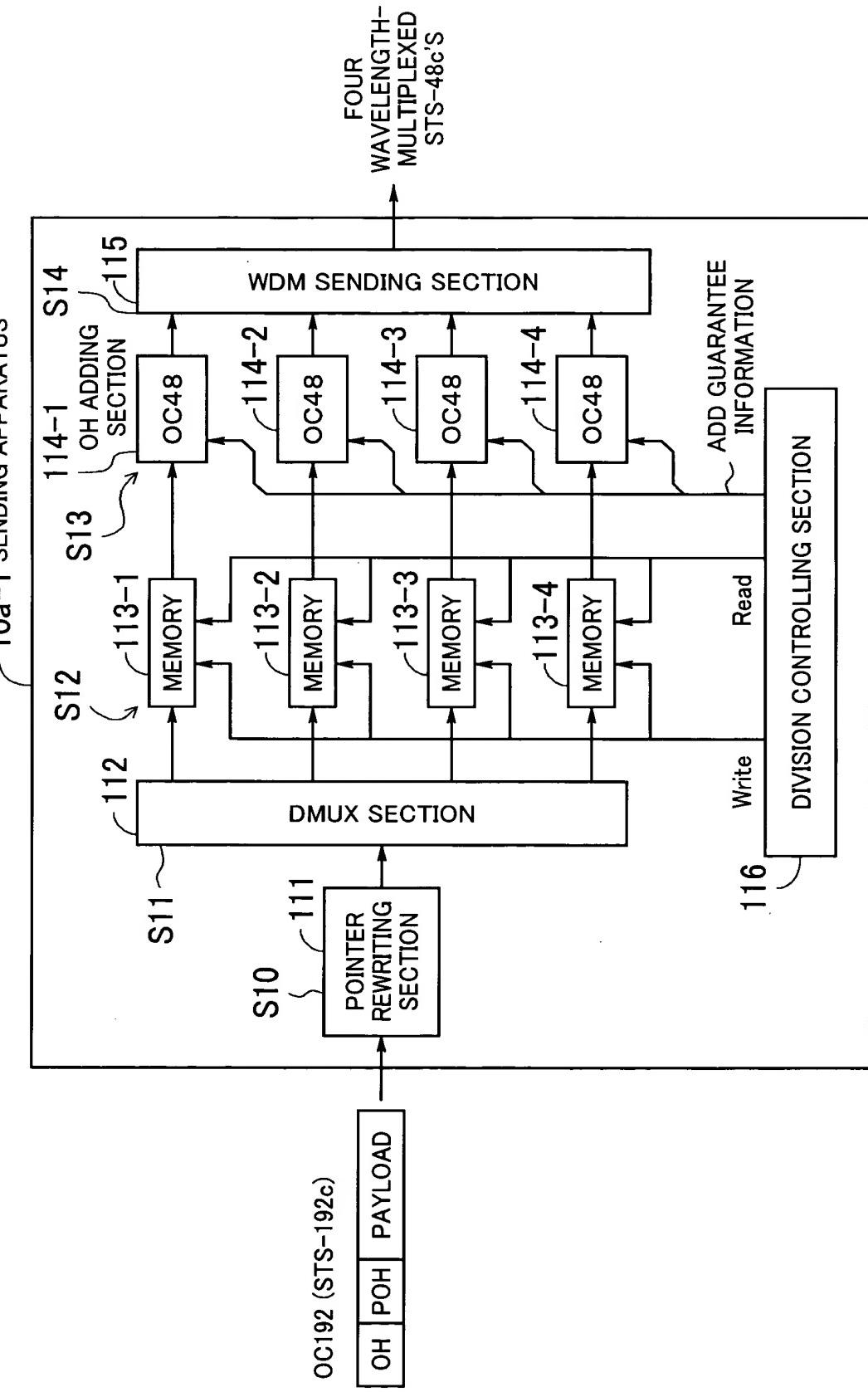


FIGURE 20a-1 RECEIVING APPARATUS

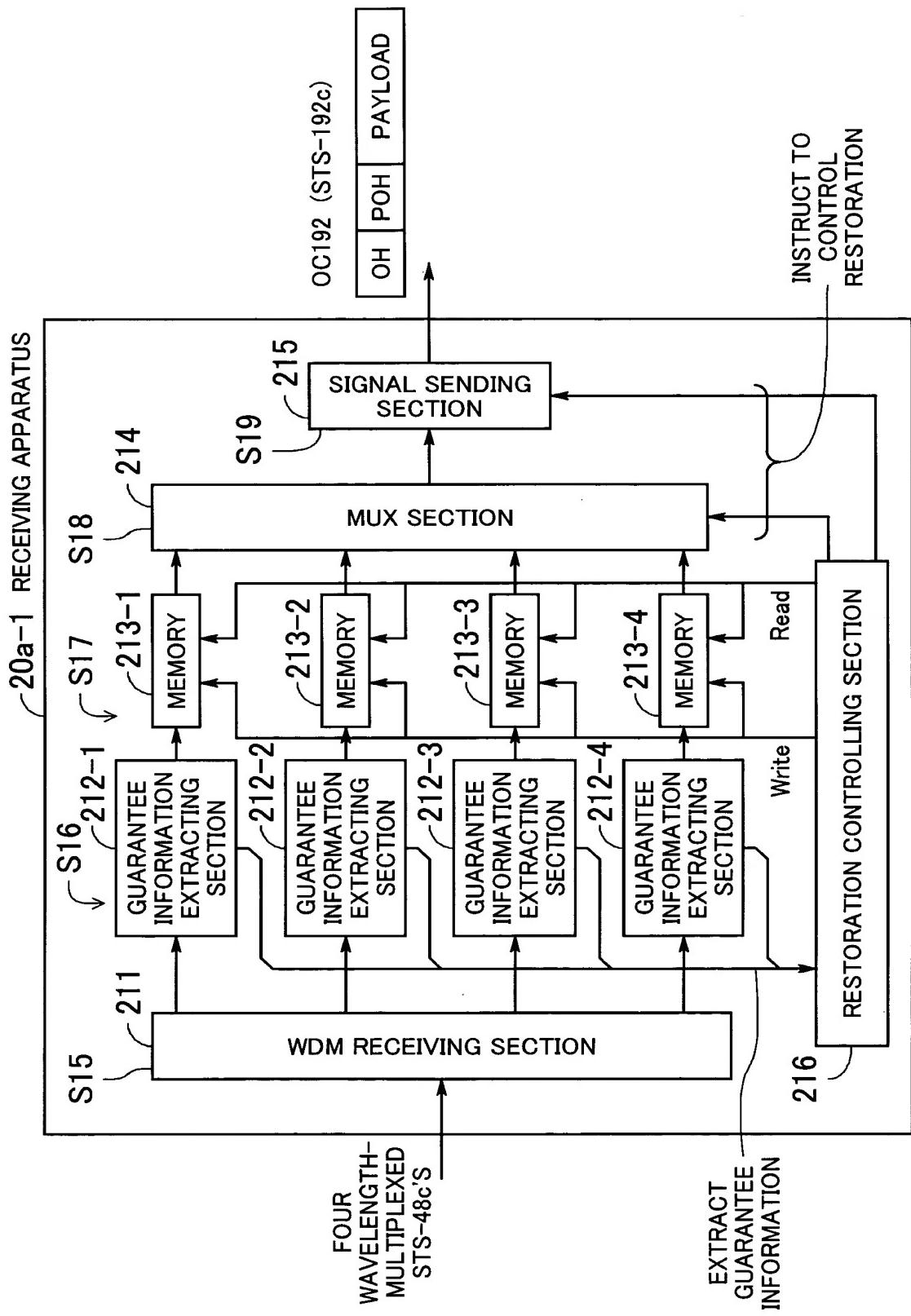
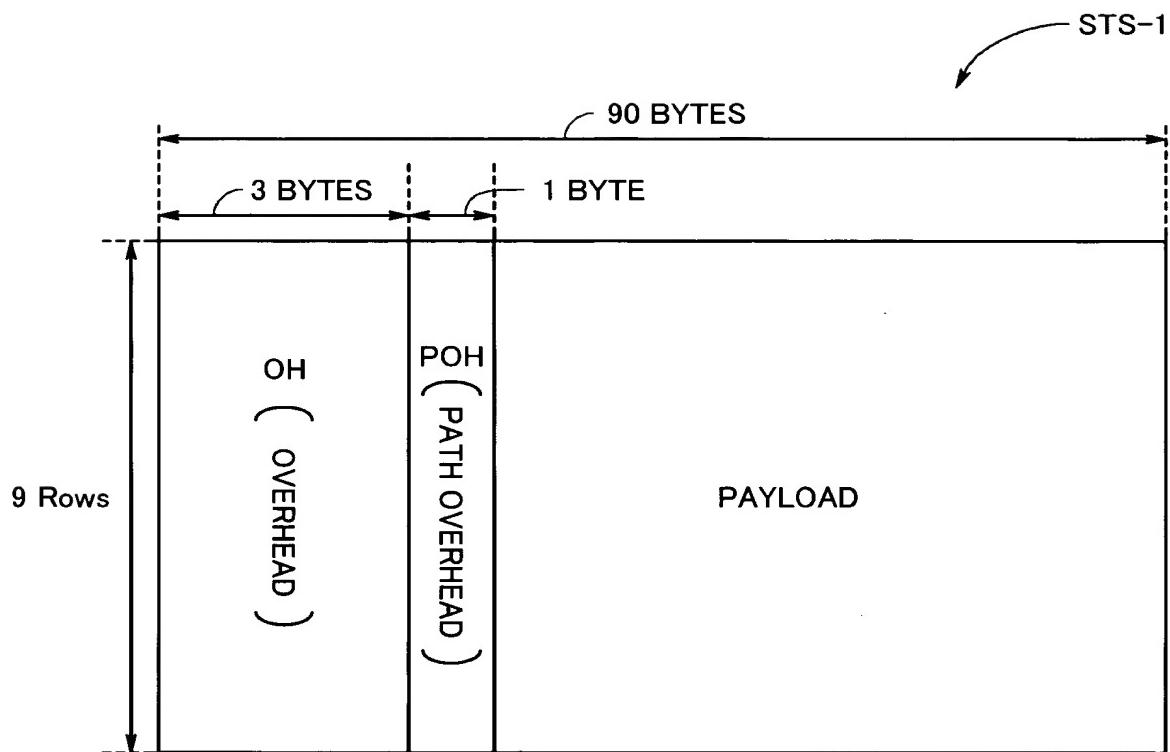


FIG. 19



**FIG. 20
PRIOR ART**

09896263 - 05521041

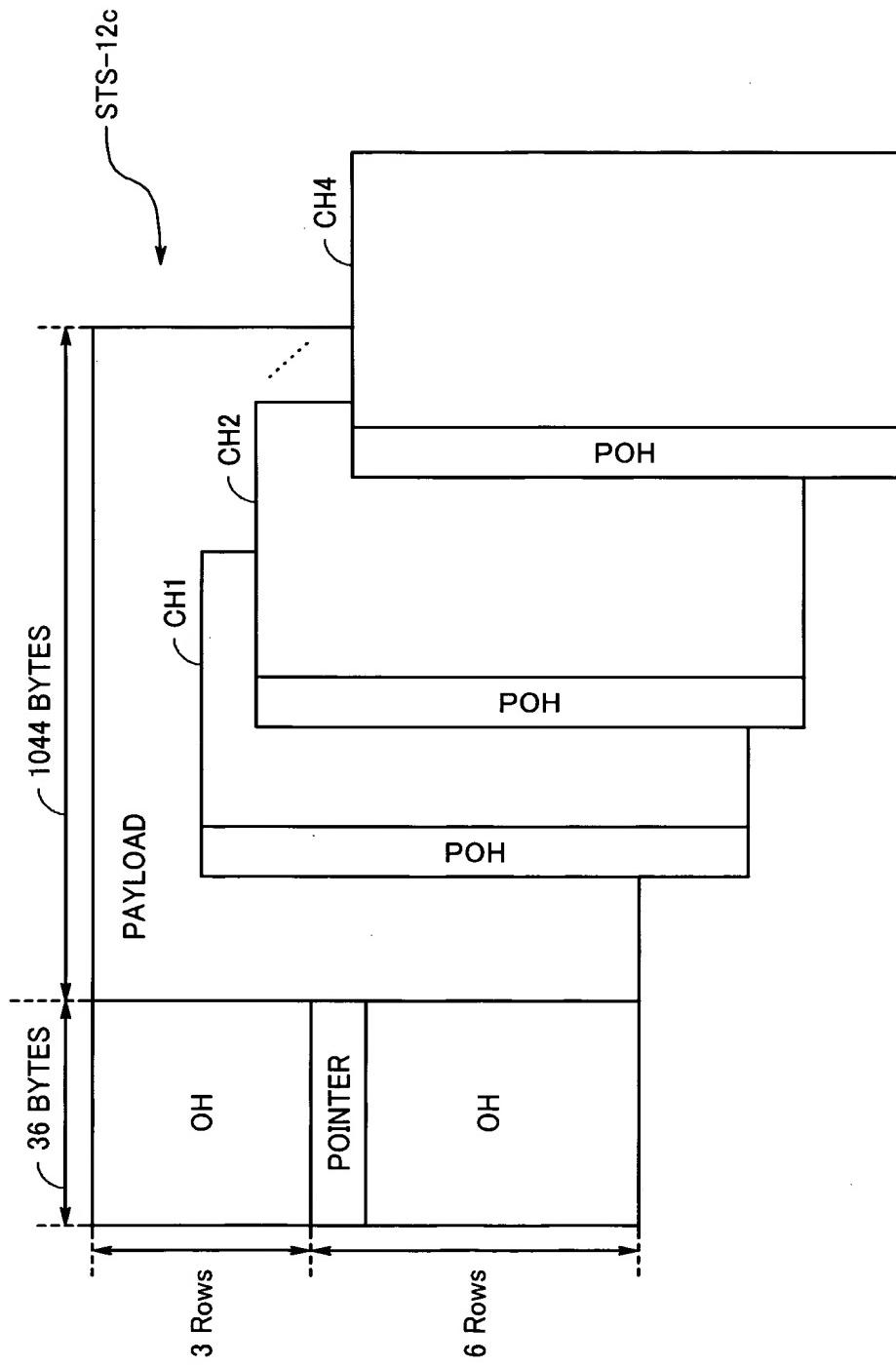


FIG. 21
PRIOR ART